IN THE SPECIFICATION

Please amend the paragraphs of the specification as follows:

On page 2, please replace paragraph [1008] with the following paragraph:

Aspects of the invention provide techniques to test the performance of terminals and access points in CDMA systems. A framework of protocols and messages is provided to support performance testing of terminals, and this framework ensures interface compatibility. In an embodiment, the framework comprises a ~~Forward Test Application Protocol~~ forward test application protocol (FTAP) for testing forward channels and a ~~Reverse Test Application Protocol~~ reverse test application protocol (RTAP) for testing reverse channels. The FTAP supports the testing of a ~~Forward Traffic Channel~~ forward traffic channel (FTC) and the collection, logging, and reporting of various statistics that may be used to determine performance, and the RTAP supports the testing of a Reverse Traffic Channel and the collection of associated statistics.

On page 4, please replace paragraph [1022] with the following paragraph:

FIG. 1 is a diagram of a wireless data communication system 100 wherein various aspects and embodiments of the invention may be implemented. System 100 provides communication for a number of cells 102, with each cell being serviced by a corresponding access point 104. An access point may also be referred to as a base station, a base-station transceiver system (BTS), or a Node B. Various terminals 106 are dispersed throughout the system. A terminal may also be referred to as an access terminal, a remote terminal, a mobile station, or user equipment (UE).

On page 6, please replace paragraph [1031] with the following paragraph:

FIG. 3 is a diagram of a forward link transmission scheme used for high rate packet data in cdma2000. Each access point transmits packet data to the terminals that have elected to receive data from the access point, based on signal strength, one at a time, in a time-division multiplexed manner. An access point transmits packet data to a terminal at or near the peak transmit power level, if at all. Whenever a terminal desires a data transmission, it sends a packet

data request in the form of a ~~Data Rate Control~~ data rate control (DRC) message to a selected access point. The terminal measures the signal quality of the forward link signals (e.g., the pilots) received from a number of access points, determines the access point having the best received signal quality (i.e., the selected access point), identifies the highest data rate supported by the best received link, and sends a DRC value indicative of the identified data rate. The DRC value is transmitted on a DRC Channel and directed to the selected access point via the use of a DRC cover assigned to the access point. The selected access point (or serving sector) schedules data transmission to the terminal on the Forward Traffic Channel according to its scheduling policy that may take into account various factors such as the DRC value received, data in the queue, and so on. Based on the status of the received data transmission, the terminal sends acknowledgments (ACKs) and negative acknowledgments (NACKs) on an ACK Channel to the selected access point. Details of the high rate packet data transmission scheme for cdma2000 is described in 3GPP2 C.S0024, entitled "cdma2000 High Rate Packet Data Air Interface Specification," hereinafter referred to as the HAI Document, and incorporated herein by reference.

On page 8, please replace paragraph [1034] with the following paragraph:

In an aspect, a framework is provided to enable testing of various elements of a CDMA system (e.g., the cdma2000 HAI system). The framework, which is referred to herein as the "Test Application Protocol" (TAP), comprises a ~~Forward Test Application Protocol~~ forward test application protocol (FTAP) for testing forward channels and a ~~Reverse Test Application Protocol~~ reverse test application protocol (RTAP) for testing reverse channels.

On page 8, please replace paragraph [1036] with the following paragraph:

In an embodiment, the RTAP (1) provides procedures and messages to control and configure the ~~Reverse Traffic Channel~~ reverse traffic channel (RTC), and (2) specifies the generation of test packets sent on the ~~Reverse Traffic Channel~~ RTC for testing that channel. Fewer, additional, and/or different capabilities may also be supported by the RTAP, and this is within the scope of the invention.

On page 9, please replace paragraph [1041] with the following paragraph:

The FTAP supports the testing of different types of forward channels. The particular channels to be tested may be individually selected, and the selected channels may be tested concurrently. In an embodiment, the FTAP supports testing of the Forward Traffic Channel, the ~~Forward~~ forward medium access control (MAC) channels, the DRC ~~Channel~~ channel, and the ACK ~~Channel~~ channel. Table 1 lists various modes supported by the FTAP. Fewer, additional, and/or different modes may also be supported, and this is within the scope of the invention.

On page 15, please replace paragraph [1062] with the following paragraph:

In an embodiment, the *FTAPParameterAssignment* message is sent on the ~~Control Channel~~ control channel (CC) and the ~~Forward Traffic Channel (FTC)~~ FTC addressed to the terminal (unicast addressing) with the ~~Signaling layer protocol~~ signaling layer protocol (SLP) set to reliable and the transmission priority set to 40.

On page 27, please replace paragraph [1098] with the following paragraph:

Referring back to FIG. 2B, at terminal 106, RX data processor 260 may be operated to process the FTAP Test packets and to forward the packets via ~~multiplexer~~ demultiplexer 262 to controller 270. Controller 270 then identifies and extracts various types of information from each received FTAP Test packet (e.g., the serving sector, the sequence number, and the length of each FTAP Test packet). Controller 270 then forms the FTAP Loop Back packets having the pertinent information as described above. The FTAP Loop Back packets may be stored in Loop Back buffer 278. At the appropriate time, the FTAP Loop Back packets are retrieved from buffer 278, routed through multiplexer 284, and processed by TX data processor 286 for transmission over the Reverse Traffic Channel.

On page 38, please replace paragraph [1141] with the following paragraph:

For a software implementation, the elements used for testing and statistics collection may be implemented with modules (e.g., procedures, functions, and so on) that perform the functions described herein. The software codes may be stored in a memory unit (e.g., memories 222 and 272 in FIGS. 2A and 2B) and executed by a processor (e.g., controllers 220 and 270 in FIGS. 2A

and 2B). The memory unit may be implemented within the processor or external to the processor, in which case it can be communicatively coupled to the processor via various means as it is known in the art.